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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER

CHOE, HENRY

ART UNIT PAPER NUMBER

2817

DATE MAILED: 10/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/631,931

Applicant(s)

MECK, RONALD A.

Examiner

Henry K Choe

Art Unit

2817



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 30 July 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 8-25 is/are allowed.
- 6) ☒ Claim(s) 1-5 and 26 is/are rejected.
- 7) ☒ Claim(s) 6,7 and 27 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 30 July 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)             | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948)     | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-5 and 26 are rejected under 35 U.S.C. 102(e) as being anticipated by Yamamoto et al (Fig. 1).

Regarding claim 1, Yamamoto et al (Fig. 1) discloses a high frequency circuit comprising an amplifier circuit (Fig. 1) having two amplifier stages (11, 21), and a power distribution network (31, 23, 22, 37, 12, 13) includes a pie C-R-C (13, 37, 23) network coupled to an inductive load (12).

Regarding claims 2-5, the limitations recited in the claims are intended use of the invention.

Regarding claim 26, Yamamoto et al (Fig. 1) discloses a high frequency circuit comprising a first power distribution branch (31, 23, 37, 13, 12) which distributes a power supply voltage (a voltage applying to a terminal 31) to an input stage (11) of a multistage amplifier circuit (11, 21), a second power distribution branch (31, 22) which distributes the power supply voltage (a voltage applying to a terminal 31) to an output stage (21), and wherein the first power distribution branch (31, 23, 37, 13, 12) includes

Art Unit: 2817

a resistor (37) having a first end (right terminal of 37) which is configured to receive the power supply voltage (a voltage applying to a terminal 31), an inductor (12) having a first end (lower terminal of 12) which is coupled to the first amplifier stage (11) and a second end (upper terminal of 12) which is coupled to a second end (left terminal of 37) of the resistor (37), a first capacitor (23) having a first end (upper terminal of 23) which is configured to receive the power supply voltage (a voltage applying to a terminal 31) and a second end (lower terminal of 23) which is coupled to a ground (ground), and a second capacitor (13) having a first end (upper terminal of 13) which is coupled to the second end (left terminal of 37) of the resistor (37) and a second end (bottom terminal of 13) which is coupled to the ground (ground).

### ***Allowable Subject Matter***

Claims 6, 7 and 27 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### ***Reasons for Allowance***

Claims 8-25 are allowed.

The following is a statement of reasons for the indication of allowable subject matter: Regarding claim 6, the closest prior art of record, Yamamoto et al (Fig. 1) does not disclose the following limitations: the bias circuits having a power input and a bias signal output. Regarding claims 8, 19 and 27, the closest prior art of record, Yamamoto

Art Unit: 2817


et al (Fig. 1) does not disclose the following limitations: one intermediate amplifier stage and its functional limitations. Regarding claims 15, 17 and 23, the closest prior art of record, Yamamoto et al (Fig. 1) does not disclose the following limitations: receiving a second power supply voltage at a second power supply node.

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (6,320,468; 6,501,335) are the cascade amplifiers with the pie networks.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-1760.

  
**HENRY CHOE**  
**PRIMARY EXAMINER**

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